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The Effect of DC Component on CMOS Injection-Coupled LC Quadrature Oscillator (IC-QO)

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Abstract

This paper creates a different insight to improve phase noise of Injection-Coupled quadrature oscillators (OOs). In fact, there are several phase noise functions and the important parameter is carrier power that considered here. The QO is analyzed and the mismatches between LC tanks that are the main proofs of phase error in this oscillator are shown. The main aim of this paper is focused on the reduction of phase noise by considering DC term. It is shown that the DC level which ignored in the most previous works is also important to improve phase noise by the carrier power. With due attention in the previous equations the phase noise can be reduced and the phase error can be cancelled or controlled by adjusting bias current. On the other word as a result, is obtained that increasing of the drain current and the voltage of LC tank decrease the phase noise and the phase error simultaneously. To confirm the proposed idea and analysis, a o,o GHz QO is designed and simulated using ',\hum TSMC CMOS technology. The simulation results show confirmation of the proposed idea.

Keywords: Quadrature, Phase Noise, Carrier Power, DC Component, Phase Error

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Introduction

Quadrature outputs of LC oscillators are the most important sections of many communication systems (Razavi, B., 1997). A more common method to produce quadrature outputs is IC-QO mechanism, because of its lower phase noise than the other types (Chamas, I. R. and Raman, S., Y · · 9). The Barkhausen's phase criterion implies the 9 · degree phase difference between IC-QO outputs. In an LC oscillator, the noise is originated in three blocks: the lossy LC tank, the transistors of differential pair and the tail current source (Lee, T. H., Y · · §). The quadrature LC oscillator without any mismatch have compensation of losses, and the argument of impedances R, L, C, gm⁻¹ for phase $\pm \pi/\Upsilon$ with no phase error, but in two coupled oscillators with mismatches, we don't have full compensation of losses and this is a quadrature error, $\Delta \varphi$. But the important point is considering the drain current that contains DC current. The phase noise of each side reduced in this work by consideration of DC term that this term in the most cases of studies is ignored. It can be seen how it will improve phase noise, phase error and even effects on some mismatches here. This paper is organized as follows: The LC Quadrature Outputs and Phase Error, introduces the basic of LC quadrature outputs and the relation between phase noise and phase error for them. In Phase Noise and Carrier Power, the phase noise of LC oscillator is analyzed and the carrier power is taken into consideration as a crucial parameter. In Drain Current and DC Component, IC-QO mechanism is considered for the idea of this paper and the drain current is adjusting the tank voltage under effects of DC current. The Proposed Idea describes how based on the previous analysis, phase noise will be reduced and as result phase error will be improved and mismatches will be lower or cancelled. To make sure, a o, o GHz source injection OO is simulated and the results will be shown.

LC Quadrature Outputs and Phase Error

Passing two differential outputs at frequency of $\Upsilon\omega$ through a frequency divider is a conventional method of generating quadrature outputs at frequency of ω . The basic of this method is very simple. The two differential outputs have a phase of Υ and therefore dividing them by Υ generates two signals with phase difference of Υ (Mazzanti A. et al, Υ · · Υ). The mismatch like device mismatches and parasitic effects cause to find out new approaches in order to reduce the phase error. Figure Υ shows the quadrature oscillator phasor diagram with mismatches. The phase difference deviates from Υ · ° because of the mismatches (Oliveira, L. B et al, Υ · · Λ).

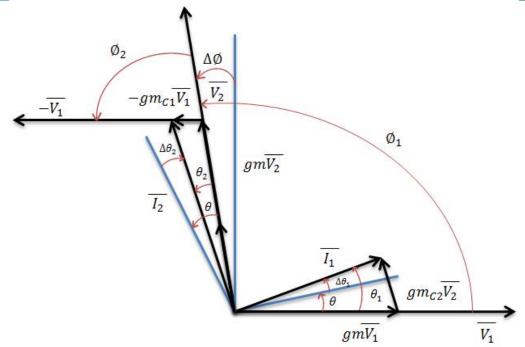


Figure \- Quadrature oscillator phasor diagram with mismatches

With considering a phase error between outputs of two oscillators following equations can be written:

$$\Delta\theta_1 = \theta_1 - \theta \tag{1}$$

$$\Delta\theta_2 = \theta_2 - \theta \tag{Y}$$

$$\theta_2 - \theta_1 = \left(\frac{\pi}{2} + \Delta\varphi\right) \tag{r}$$

An RLC circuit with high parallel resistance has a high quality factor, and a small derivation from the resonance gives a significant phase variation, as shown in fig 7. In coupled LC oscillators with high Q resonators, small mismatches produced a high quadrature error. In first quadrature oscillators with low Q integrated inductors can reach to a good quadrature relationship (Rofougaran, A. et al, \ 9 9 7). But with considering equations (Υ) and (Υ), a relationship between phase noise and phase error is obtained and implies that the phase error and the mismatch also can be reduced by the good phase difference. So a good quadrature relationship can be reached by a low phase noise, too.

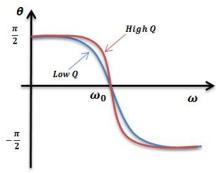


Figure Y- Phase of tank impedance for different Qs (Oliveira, L. B et al, Y··A)

Phase Noise and Carrier Power

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To consider the phase noise of coupled oscillator, it is better to investigate a single LC oscillator phase noise, first. Each differential pair switches the tail current I_T into the branch of LC resonators. This current is proportionated to the drain current of differential transistors. The transistors are ideal switchers that connect in parallel RLC tanks. The oscillator output voltage is approximately sinusoidal but the LC tank acts as a filter thus it represents as a square wave form (Rael, J. J. and Abidi, A. A., $\forall \cdot \cdot \cdot$). The voltage amplitude of LC tank can be written as (Hajimiri, A. and Lee, T. H., $\forall \cdot \cdot \cdot$):

$$V_{Tank} = \frac{4}{\pi} I_T R_P \tag{5}$$

The voltage harmonics are attenuated by LC tank in each oscillator so the impedances of inductors and the capacitors are canceled and only paralleled resistances are leaved (Rael, J. J. and Abidi, A. A., $\forall \cdot \cdot \cdot$) as shown in figure \forall .

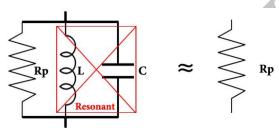


Figure \(-\) Behavior of RLC tank in resonant frequency

To calculate the phase noise contribution of tank, assuming that the noise source is the thermal noise (Lee, T. H., $\forall \cdot \cdot \cdot \xi$) and the equation (\circ) represents a current source across the tank with spectral density:

$$S(i_n) = \frac{4KT}{R_P} \tag{0}$$

And the voltage noise source is as:

$$S(v_n) = S(i_n) \cdot |Z|^2 \tag{7}$$

Where Z is the tank impedance . The small offset frequencies ω_m will be as:

$$\omega_m \ll \frac{\omega_0}{2Q}$$
 (Y)

And the impedance of LC tank is (Razavi, B., \ 9 9 \) (Lee, T. H., \ \ \ \ \ \):

$$|Z(\omega_0 + \omega_m)|^2 \approx R_P^2 \frac{1}{4Q^2} \left(\frac{\omega_0}{\omega_m}\right)^2 \tag{A}$$

The Q is defined as equation (9) (Razavi, B., 993):

$$Q = \frac{\omega_0}{2} \sqrt{\left(\frac{dA}{d\omega}\right)^2 + \left(\frac{d\theta}{d\omega}\right)^2} \tag{9}$$

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Where A=|Z (jw) |, θ =arg|Z (jw)|, $\omega = 1/\sqrt{LC}$ is the resonance frequency. In an LC oscillator dA/d ω = · and for O = O (ω) (Razavi, B., \ 9.3.):

$$Q_0 = \frac{\omega_0}{2} \left| \frac{d\theta}{d\omega} \right|_{\omega = \omega_0} = R_P \sqrt{\frac{C}{L}} = \frac{R_P}{\omega_0 L} \tag{(1.)}$$

$$Q_0 = R_P \sqrt{\frac{C}{L}} = \frac{R_P}{\omega_0 L} \tag{11}$$

Typically, the losses in the capacitors are so low and they will be ignored and because the losses in the inductors are considered, the resonator quality factor is determined mainly by inductor and the parallel resistance is obtained from the inductor quality factor (Lee, T. H., $\forall \cdot \cdot \cdot \xi$). Using equations (\forall) and (\forall):

$$S(v_n) = \frac{4KT}{R_P} \left| \frac{R_P}{2Q} \frac{\omega_0}{\omega_m} \right|^2 = 4KTR_P \left(\frac{\omega_0}{2Q\omega_m} \right)^2 \tag{17}$$

From above equation can conclude that increasing Q leads to noise spectral density reduction when all other parameters keep without any changes. It shows that the output noise is corresponding to frequency that is due to LC tank filtering and the spectral density is inversely proportional to the square of the offset frequency. This behavior is due to voltage frequency response of an RLC tank rolls off as \frac{1}{f}\$ to each side of the center frequency (Lee, T. H., and A. Hajimiri, \frac{1}{f}\$ \cdot \cd

$$\ell(\omega_m) = 10\log\left\{\frac{2FkT}{P_{Carrier}}\left[1 + \left(\frac{\omega_0}{2Q\omega_m}\right)^2\right]\left(1 + \frac{\omega_1/f^3}{|\omega_m|}\right)\right\}$$
 (17)

Where k is Boltzmann constant, T is absolute temperature, $P_{Carrier}$ is carrier power dissipated in the resistive part of the tank, ω is oscillation frequency, Q is quality factor, ω_m is offset from the carrier, ω_1/f^{τ} is corner frequency between $1/f^{\tau}$ and $1/f^{\tau}$ zones of the noise spectrum and F is empirical parameter called excess noise factor which includes nonlinear effects for LC oscillators (Leenaerts,D. et al, $1/f^{\tau}$). The phase noise is usually obtained from division by the carrier power as shown in equation $1/f^{\tau}$. The carrier power equation is as:

$$P_{Carrier} = \frac{V_{Tank}^2}{R_P} \tag{15}$$

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that is used in this paper is the voltage of LC tank and the power that is in direct proportional to the square of this voltage.

Drain Current and DC Component

In this work, the IC-QO mechanism is considered because it can acts as a combination of two frequency dividers and also two frequency doublers so that the coupling transistor pairs operate as frequency doublers (Chamas, I. R. and Raman, S., Y··٩). Figure ξ represents a frequency divider diagram and operation.

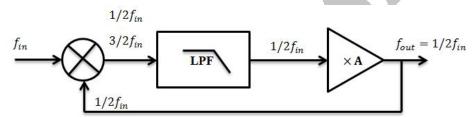


Figure 4-Block diagram of a frequency divider

The injection current of IC-QOs can be calculated by the equation (\ \ \circ\):

$$I_{inj(1,2)} = \frac{K_{p,n}}{2} (V_{m(1,2)} - V_{th})^2 \tag{10}$$

$$I_{inj(1,2)}(t) = I_m Cos(2\omega t + \phi_{1,2}) + I_m$$
(17)

$$I_m = \frac{I_{inj(1,2)}}{2} \tag{YY}$$

Where ϕ_1 and ϕ_2 are the phases of the second-order frequency component of injection current in oscillators. In equation () 7), the injection current is considered as sum of a DC level and a sinusoidal waveform. Sinusoidal approximation of the injection current has been used in previous works but the DC term is ignored (Chamas, I. R. and Raman, S., $\Upsilon \cdot \cdot \Upsilon$). But this section shows that the DC component directly contributes in calculations and effects on outputs (Ghonoodi, H. and Miar-Naimi, H., $\Upsilon \cdot \Upsilon \Upsilon$).

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The differential structure of cross-coupled causes a frequency of twice oscillator frequency for the injection current at the source. The IC-QO topology generates four quadrature outputs but because of the mismatches, the relative phase difference deviates from 9.9° .

Assuming the phase error of $\Delta \phi$ from quadrature condition between outputs of two oscillators, so following equations could be written as:

$$V_{1}(t) = V_{m(1)}Cos(\omega t + \theta_{1}) \tag{1.1}$$

$$V_2(t) = V_{m(2)}Cos(\omega t + \theta_2) \tag{19}$$

$$\theta_2 - \theta_1 = \left(\frac{\pi}{2} + \Delta\varphi\right) \tag{Υ}.$$

Where ω is angular frequency and $\Delta \phi$ is phase deviation amount from $\mathfrak{q} \cdot \mathfrak{o}$. If assumed that the currents inject to the source node, they will be switched by the cross coupled core transistors, as if they are multiplied by a square wave of frequency of ω . When the switching transistors are turned on, they conduct the whole source current. The drain current of switching pairs with considering the DC term can be written as:

$$I_{d(1)}(t) = I_{mix1(1)}(t) + I_{mix2(1)}(t) + I_{dc(1)}(t)$$

$$(Y1)$$

$$I_{d(2)}(t) = I_{mix1(2)}(t) + I_{mix2(2)}(t) + I_{dc(2)}(t)$$
 (YY)

According to equations (Υ) and (Υ) drain currents consist of I_{mix}) (t), I_{mix} (t) and I_{dc} current for both oscillators and they are source injected current, capacitor current and DC current respectively. Referring to (GHonoodi, H. and Naimi, H. M., Υ)) with considering Fourier's theory I_{dc} can be written as:

$$I_{dc(1,2)}(t) = \frac{2}{\pi} (I_{B(1,2)} - I_{m(1,2)}) Cos(\omega t + \theta_{1,2})$$
(YT)

Note that the DC current at the source node consists of the tail bias current and injection current, while I_{mix} (t) in equations (Υ) and (Υ) is the drain current raised by injection current and indeed, I_{mix} (t) is derived from the multiplying the source current and the square wave due to gate voltage (Ghonoodi, H. and Miar-Naimi, H., Υ) Υ). As resultant, if Z is considered as the impedance of tank, the voltage of each LC tanks will be as:

$$V_{1\pm} = -I_{d(1\pm)}.Z_1(j\omega) \tag{7.5}$$

$$V_{2\pm} = -I_{d(2\pm)} Z_2(j\omega) \tag{Υ o)}$$

Where $V\pm$ is the phasor voltage of LC tank, $V_{1,\,7}$ (t) and $I_{d\pm}$ is the phasor of drain current, $I_{d\,1,\,7}$ (t), Also the phasor relationship between drain current and voltage of tank can be as equation ($7\,7$) (Ghonoodi, H. and Miar-Naimi, H., $7\,\cdot\,7$):

$$\angle V_{(1,2)}(t) = \angle I_{d(1,2)}(t) + \angle Z_{L(1,2)}(j\omega)$$
 (Y7)

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So the DC current is valuable in calculating of the drain current. Considering DC current causes the drain current increases and this leads to voltage of tank to be increased according to represented equations.

In this section, it is shown that the amplitude of drain current raise by the injection current, capacitor current and DC current that causes the better performance of tank voltage. The important role of the DC level for drain current and V_{tank} observed here as another significant point for the purposed idea of this paper.

The Proposed Idea

$$\ell(\omega_m) = \frac{P(\omega_m)}{P(\omega_0)} \tag{YY}$$

Where $P(\omega_m)$ is the single sideband noise power at a distance of ω_m from the carrier (ω_*) in \ Hz bandwidth, $P(\omega_*)$ is the carrier power (Oliveira, L. B et al, \ \cdot \cdot \cdot \lambda). Regards to most of the phasenoise equations can conclude that whenever carrier power increased the phase noise will be decreased. According to equation (\ \cdot \cdot \cdot) and they are in inversely proportional to each other. As mentioned in equation (\ \cdot \cdot) the carrier power is in direct relation with the voltage of LC tank.

The effects of DC current on drain current and voltage of tank have been shown by equations (Υ), (Υ) (Note that V_{Υ} is V_{tank}). The equations (Υ) and (Υ) also show that the drain currents are affected by this DC component.

From the results of analysis in previous sections, considering the DC term causes increasing of the carrier power and based on the phase noise equations, the phase noise can be reduced and also the outputs will be improved respectively. In previous works the relationship between phase difference and $\Delta \varphi$ were obtained ($\Delta \varphi \propto \Delta \theta$) in some cases, (Oliveira, L. B et al, $\Upsilon \cdot \cdot \cdot \wedge$) and also refers to equations (Υ) and ($\Upsilon \cdot$), $\theta \cdot -\theta \cdot = (\pi/\Upsilon + \Delta \varphi)$ phase error is in direct relation with phase differences of two sides Υ and Υ , so the reduction of phase noise of each oscillator causes the better phase errors, thus the mismatches will be lower in some cases and small mismatches will be removed.

Simulation Results

As described in the Drain Current and DC Component, Injection-Coupled Quadrature Oscillators can be considered as a combination of two frequency dividers and two frequency doublers which the coupling transistor pairs works as frequency doublers. In IC-QO structure, the NMOS and PMOS have similar mechanism, but PMOS coupling has several advantages over NMOS coupling and the main advantage is that, at the source node there is no current sharing.

In order to simulate the result of study, the IC-QO in figure \circ was implemented. The transistors Mcp\ and Mcp\ are the coupling transistors which generate the frequency of $\forall \omega$ (twice the oscillation frequency) and inject the current to the source of Msw\ and Msw\ as oscillation core component.

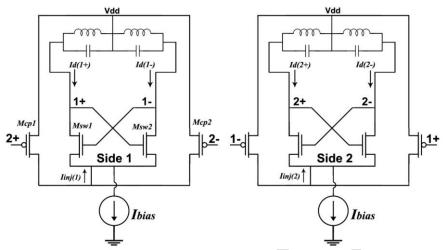
 

Figure • - Proposed CMOS Injection-Coupled LC Quadrature Oscillator

As a result, it is apparent in figure 7 that with increasing the DC level, the value of Phase Noise will be decreases. As well as, with decreasing the phase noise, the phase error will decrease respectively and also small mismatches are cancelled as is observable in figure Y.

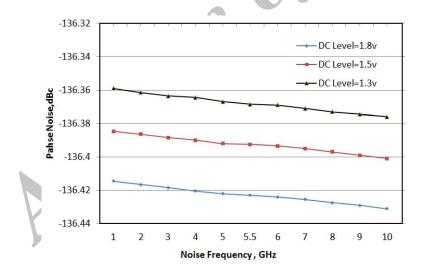


Figure ₹ – Plot of Phase Noise with DC level changes

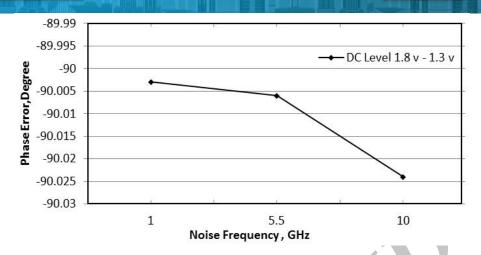


Figure ∨ – Plot of Phase Error reduction respect to the Phase Noise

Conclusion

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